

# ACKNOWLEDGEMENT RECEIPT

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## Title of Invention

METHOD OF FABRICATING AN INTEGRATED CIRCUIT  
THROUGH UTILIZING METAL LAYERS TO PROGRAM  
RANDOMLY POSITIONED BASIC UNITS

Submission Type : Utility Patent Filing

Application Number:

10/708256



EFS ID:

55822

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First Named Applicant: Hsin-Shih Wang

Attorney Docket Number: FTCP0031USA

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-power-of-attorney-grant	FTCP0031-uspoat.xml	1045	2004-02-19
us-power-of-attorney-grant	us-power-of-attorney-grant.dtd	4923	2004-02-19
us-power-of-attorney-grant	us-power-of-attorney-grant.xsl	12448	2004-02-19
us-assignment	FTCP0031-usasn.xml	1490	2004-02-19
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us-request	FTCP0031-usrequ.xml	1542	2004-02-19
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us-declaration	FTCP0031-usdecl.xml	706	2004-02-19
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us-fee-sheet	FTCP0031-usfees.xml	2097	2004-02-19
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application-body	isoamsb.ent	3988	2004-02-19
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